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PTO/SB/21 (12/97)

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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE



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TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Application Number	10/719,673
Filing Date	November-21, 2003
First Named Inventor	Khosro Khakzadi
Group Art Unit	2121
Examiner Name	
Total number of pages in this submission	3
Attorney Docket Number	L13.12-0251 / 03-1862

ENCLOSURES (check all that apply)

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<input type="checkbox"/> Amendment/Response	<input type="checkbox"/> Licensing-related Paper	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
<input type="checkbox"/> After Final	<input type="checkbox"/> Petition Routing Slip (PTO/SB/69) and Accompanying Petition	<input type="checkbox"/> Proprietary Information
<input type="checkbox"/> Affidavits(s)/declaration(s)	<input type="checkbox"/> To Convert a Provisional Application	<input type="checkbox"/> Status Letter
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<input checked="" type="checkbox"/> Information Disclosure Statemen	<input type="checkbox"/> Small Entity Statement	
<input type="checkbox"/> Certified Copy of Priority Document(s)	<input type="checkbox"/> Request for Refund	
<input type="checkbox"/> Response to Missing Parts/ Incomplete Application	Remarks	
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	Enclosures: JP01202397 JP02202886	

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Individual Name	Sandeep Jaggi, Reg. No. 43,331, Phone: [+1] 408-433-7191
Signature	<i>[Signature]</i>
Date	1/26/05

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date:

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Khosro Khakzadi et. al.

Serial No. : 10/719,673

Filed : November 21, 2003

Chip Design Command Processor



§ §

Group Art Unit : 2121

Examiner :

Atty Docket : L13.12-0251 / 03-1862

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Date: 1/26/05

Respectfully submitted,



Sandeep Jaggi

Reg. No. 43,331



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PTO/SB/08A (10-96)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known

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First Named Inventor	Khosro Khakzadi
Group Art Unit	2121
Examiner Name	
Attorney Docket No.	L13.12-0251 / 03-1862

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code (if known)			
		2002/00733		Cooke et al.	06-13-2002	
		4,656,592		Spaenenburg et al.	04-07-1987	
		5,818,728		Yoeli et al.	10-06-1998	
		5,818,729		Wang et al.	10-06-1998	
		6,459,136		Amarilio et al.	10-01-2002	

FOREIGN. PATENT DOCUMENTS

Examiner Initials	Cite No.	Foreign Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code (if known)			
		JP JP 01202397		Nishi, Hiroaki	07-27-2001	
		JP JP 02202886		Seki, Takeo	07-19-2002	

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